

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bor-Sung LIANG
Appl. No. : 10/695,812
Filed : October 30, 2003
Title : Processor And Method Capable Of Executing Conditional Instructions

TC/A.U. : 2183
Examiner : Unassigned

Docket No.: : LIAN3019/EM
Customer No: : 23364



Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to Rule 37 C.F.R. §1.51(b), §1.56, §1.97, and §1.98, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper.

This Information Disclosure Statement is submitted prior to the mailing date of the first Office Action on the merits received by Applicant in the above identified application.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

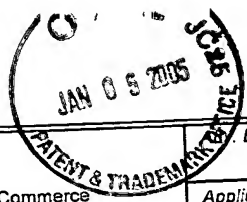
Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 02-0200.

Respectfully submitted,

Eugene Mar
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Date: January 5, 2005

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B/O Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	Docket Number LIAN3019/EM	Serial Number 10/695,812
	Applicant Bor-Sung LIANG	
	Filing Date October 30, 2003	Group 2183

U.S. Patent Documents

Examiner Initial	Document Number	Date	Patentee/Applicant	Class	Subclass	Filing Date if Appropriate
	2002/0178345	11/28/2002	Drabenstott et. al.			04/01/2002
	2003/0061471	03/27/2003	Matsuo			11/01/2002

Foreign Patent Documents

Examiner Initial	Document Number	Publication Date	Country/Agency	Class	Subclass	Translation	
						Yes	No
	1 310 864 A2	05/14/2003	EP				

Other Documents (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

	TriCore, 32-bit Unified Processor, DSP Optimization Guide, Part 1: Instruction Set; vol. 1.6.4; January 2003; pp. 1-4.

Examiner	Date Considered
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.